

Electronic Acknowledgement Receipt

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| EFS ID: | 4087477 |
| Application Number: | 09328910 |
| International Application Number: | |
| Confirmation Number: | 7455 |
| Title of Invention: | LEADFRAME SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE USING THE SAME AND METHOD OF AND PROCESS FOR FABRICATING THE TWO |
| First Named Inventor/Applicant Name: | YUJIRO KAJIHARA |
| Customer Number: | 20457 |
| Filer: | Paul J. Skwierawski/Stacey Keaton |
| Filer Authorized By: | Paul J. Skwierawski |
| Attorney Docket Number: | 1374.32049R00 |
| Receipt Date: | 09-OCT-2008 |
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| Time Stamp: | 14:52:16 |
| Application Type: | Utility under 35 USC 111(a) |

Payment information:

| | |
|--|-------------|
| Submitted with Payment | yes |
| Payment Type | Credit Card |
| Payment was successfully received in RAM | \$ 180 |
| RAM confirmation Number | 112 |
| Deposit Account | |
| Authorized User | |

File Listing:

| Document Number | Document Description | File Name | File Size(Bytes)/ Message Digest | Multi Part /.zip | Pages (if appl.) |
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|--|---|------------------|--|----|---|
| 1 | Information Disclosure Statement Letter | 32049R00IDS.pdf | 59983 16ea04bf49ca3c30ba2f100cabee643514a5f765 | no | 2 |
| Warnings: | | | | | |
| Information: | | | | | |
| 2 | Information Disclosure Statement (IDS) Filed (SB/08) | 32049R00SB08.pdf | 130184 cfacf7d440241440a1d028a02c1b0e72b7d1738e | no | 1 |
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| 3 | Information Disclosure Statement Letter | 32049OA.pdf | 333994 2f1849138e6f570c20e8a4959c727568dd091116 | no | 5 |
| Warnings: | | | | | |
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| 4 | Foreign Reference | 32049REF.pdf | 268195 743c5461f471aa3bf12cf53250ecd273d96e2a8 | no | 8 |
| Warnings: | | | | | |
| Information: | | | | | |
| 5 | Fee Worksheet (PTO-06) | fee-info.pdf | 30783 59115203fbaff0d1652990b888405d70e14ddd47 | no | 2 |
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| Total Files Size (in bytes): | | | 823139 | | |
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